

## RESTORATION OF SUDDEN VOLTAGE SAGS OF A SENSITIVE LOAD IN A REAL NETWORK USING DVR

**M. Farsadi<sup>1</sup> J. Mostafapour<sup>2,4</sup> E. Ogabi<sup>3,4</sup>**

*1. Electrical Engineering Department, Urmia University, Urmia, Iran, m\_farsadi@urmia.ac.ir*

*2. Azarbaijan Regional Electric Company, Urmia, Iran, jafarmostafapour@yahoo.com*

*3. West Azarbaijan Electric Distribution Company, Urmia, Iran, ebrahim.1978@yahoo.com*

*4. West Azarbaijan Science and Research Unit, Urmia, Iran*

**Abstract-** DVR is the apparatus that connects in series to the network and via injecting voltage into the system, regulates the load. This tool has capability of producing and absorbing active and reactive power. The DVR, usually places between sensitive load source and feeders in distribution system and its main task is quick strengthening of the voltage in load direction when abrupt changes occur. In this paper, we will investigate the effect of using DVR to compensate voltage sags of a sensitive load in the distribution system of West Azerbaijan province by using Matlab/Simulink.

**Keywords:** Voltage Sag, Dynamic Voltage Restorer (DVR), FACTS, STATCOM, VSC.

### I. INTRODUCTION

In the past decade, there was a growth in the electrical equipment that has high sensitivity to the power quality. Major industrial consumers have reported many large economic losses due to the small drops of power quality. Therefore, there are a lot of attempts to prevent these losses. One of the newest solutions is using the technology of manufacturing power equipment to control power systems. A set of these equipment known as FACTS are being used to deal with problems of power distribution and continuity [1].

Today, there is wide range of flexible controllers which were manufactured by power equipment and are being used in power systems. Among them are Distribution Static Compensator (D-STATCOM) and Dynamic Voltage Restorer (DVR) that the latter is designed based on VSC and has attracted the most attention [1-4]. In this work, we have been used MATLAB software for modeling and analyzing of this controller for a sensitive load in West Azerbaijan's distribution network [5-7].

The advantage of this method is that, we only need to measure voltage and measuring reactive power is not needed [8-17]. In the case of DVR, the effects of occurring errors in system for a sensitive load in West Azerbaijan's distribution network and controlling voltage sag were simulated and analyzed.

### II. INVESTIGATING THE PERFORMANCE OF DVR IN TRANSIENT STATE

Due to the relations of permanent performance of DVR, the control strategy of DVR in permanent state can be established. Now, in this section, we will establish the control strategy of transient state of DVR in two states of balance and imbalanced source.

#### A. When Source Is Balanced

The relations established for permanent state of source are based on the assumptions that, source or network is conventional. Although, this cannot be practical, but DVR voltage can be assumed only as the measurements of local voltages. Assuming that, the voltage of left side terminal of DVR that comes from source as  $V_t$ , we have:

$$\bar{V}_t = \bar{V}_l - \bar{V}_f = \bar{V}_l - |\bar{V}_f| (a_1 - jb_1) \quad (1)$$

Assuming  $\bar{V}_l = |\bar{V}_l| < 0$ , the following relation is obtained for injected DVR voltage:

$$|\bar{V}_f|^2 - 2\alpha_1 |\bar{V}_t| \cdot |\bar{V}_f| - |\bar{V}_t|^2 - |\bar{V}_f|^2 = 0 \quad (2)$$

Therefore, we have following control algorithm:

- The phase of system values will be locked on reference.
- The average half cycle value of positive component of current line will be established concerning the previous step.
- The value of range of  $|\bar{V}_f|$  will be calculated from Equation (2).
- Finally, injected DVR voltage will be obtained from combination of range of  $|\bar{V}_f|$  and angle of current line.

#### B. When the Source Is Imbalanced and Has Fluctuations

In this case, we can assume  $V_t$  as follows:

$$V_t = V_{tp} + V_{t-rest} \quad (3)$$

where,  $V_{tp}$  is the main component (positive) of terminal voltage and  $V_{t-rest}$  is voltage shows the effects of imbalances and harmonics.



### III. RESULTS OF STUDIED SAMPLES USING MATLAB

This section shows all of the results that we have been obtained. The Figure 1 shows simulation of studied sample and Figure 2 shows equivalent circuit of distribution system. Figures 3-4 show two phase to ground error without DVR and correcting with DVR. Figure 5 shows correcting two phases to ground error with DVR which Figure 6 shows performance of DVR in correcting two phases to ground error with DVR.

Figure 7-8 show the effective value of two phases to ground without DVR and correcting the effective value of two phases to ground with DVR. Figure 9-10 show the effective value of three phases to ground voltage error without DVR and correcting the effective value of three phases to ground voltage error with DVR

Figures 11-12 show the error of one phase to ground in all three phases without DVR and correcting the error of one phase to ground in all three phases with DVR. Figures 13-14 show the effective value of one phase to ground voltage error without DVR, correcting effective value of one phase to ground voltage error with DVR.

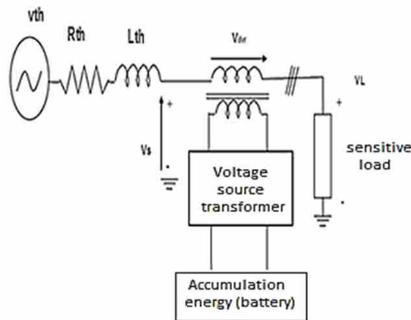


Figure 2. Equivalent circuit of distribution system

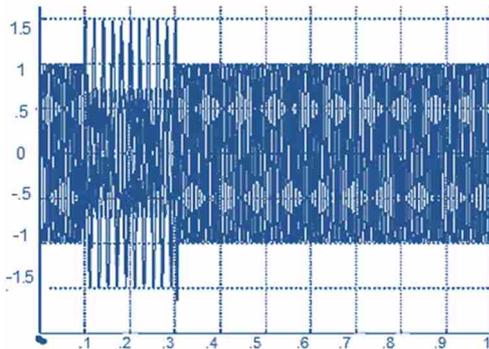


Figure 3. Two-phase to ground error without DVR

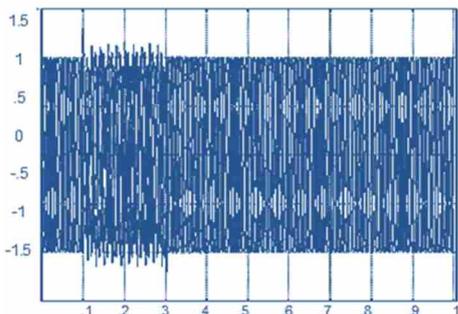


Figure 4. Correcting two-phase to ground error with DVR

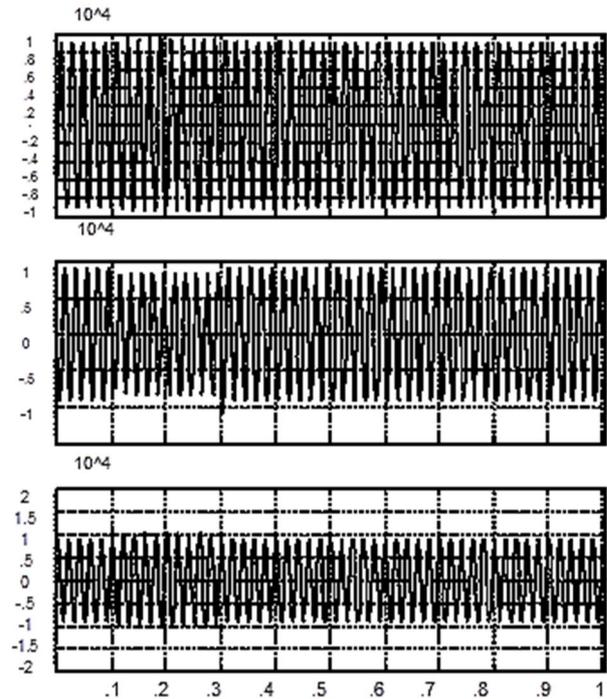


Figure 5. Correcting two-phase to ground error with DVR

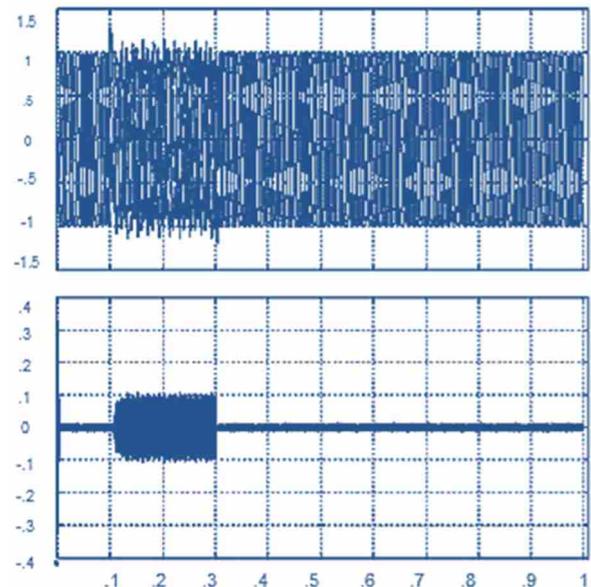


Figure 6. Performance of DVR in correcting two-phase to ground error with DVR

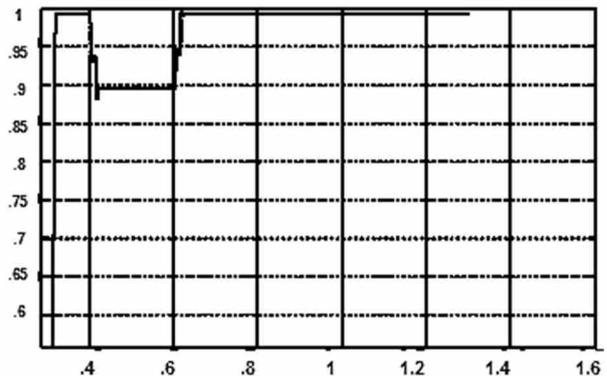


Figure 7. Effective value of two-phase to ground without DVR

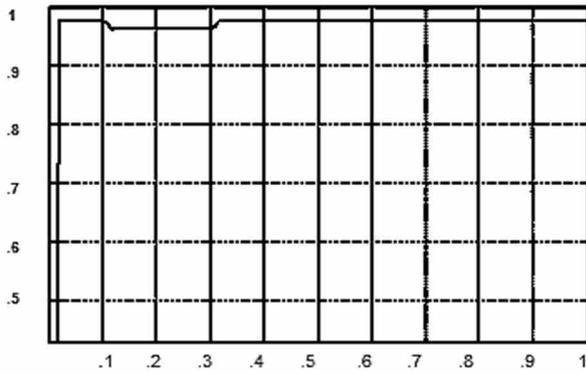


Figure 8. Correcting the effective value of two-phase to ground with DVR

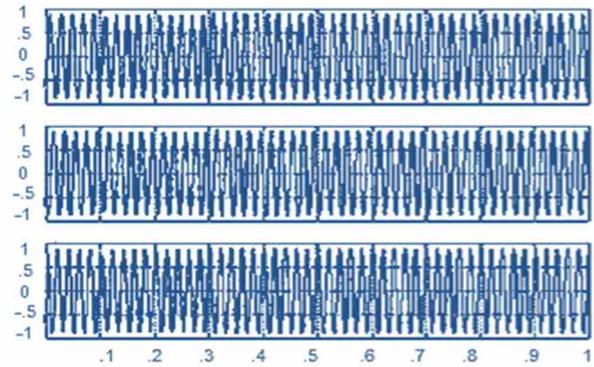


Figure 12. Correcting the error of one phase to ground in all Three-phase with DVR

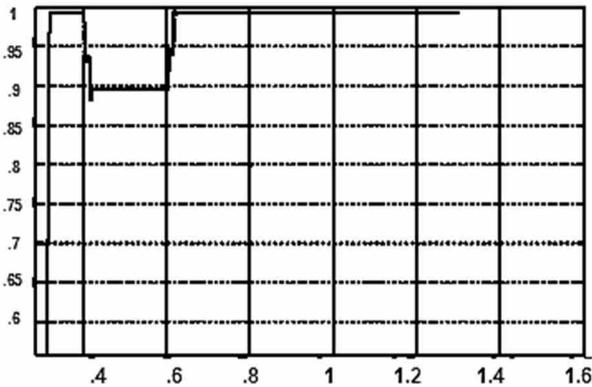


Figure 9. Effective value of three-phase to ground voltage error without DVR

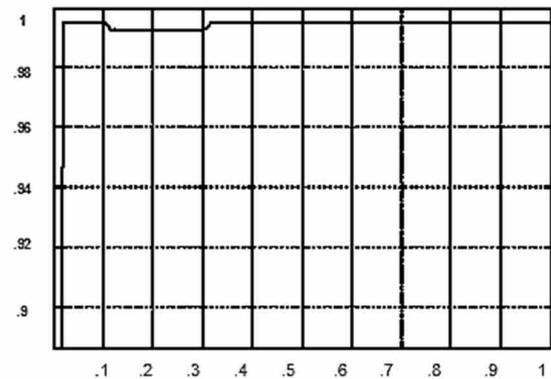


Figure 13. Effective value of one phase to ground voltage error without DVR

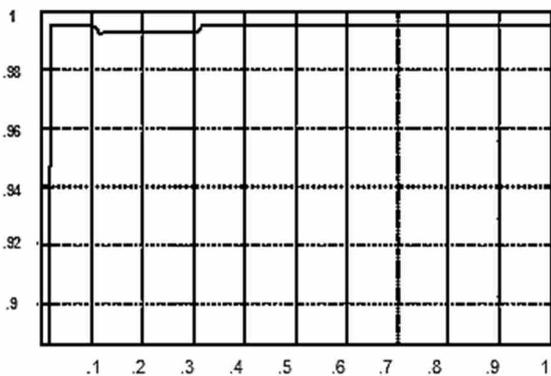


Figure 10. Correcting the effective value of three-phase to ground voltage error with DVR

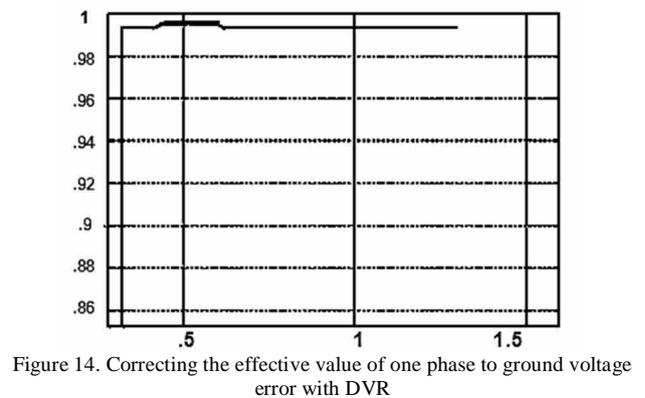


Figure 14. Correcting the effective value of one phase to ground voltage error with DVR

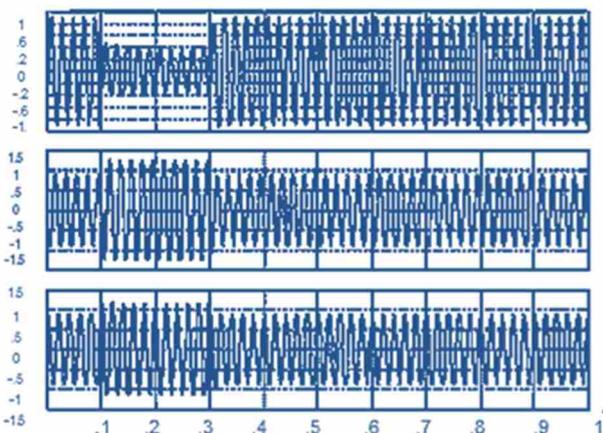


Figure 11. Error of one phase to ground in all three-phase without DVR

#### IV. CONCLUSIONS

The conducted simulation has shown that, DVR has a great capability to regulate voltage and can be used to improve the quality in sensitive loads. According to obtained results we can improve approximately 10% voltage drop in the system. So by using proper DVR, also we can increase the quality and quantity of system reactive power in the case of balance and in balance conditions during short periods.

#### REFERENCES

- [1] P. Heine, M. Lehtonen, "Voltage Sag Distributions Caused by Power System Faults", IEEE Transactions on Power Systems, Vol. 18, No. 4, November 2003.
- [2] J.H.R. Enslin, "Unified Approach to Power Quality Mitigation", Ind. Electron Proc., ISIE'98, Vol. 1, pp. 8-20, 1998.

[3] S.S. Choi, B.H. Li, D.M. Vilathgamuwa, "Dynamic Voltage Restoration with Minimum Energy Injection", IEEE Trans. on Power Systems, Vol. 15, No. 1, February 2000.

[4] D.M. Vilathgamuwa, A.A.D. Ranjith Perera, S.S. Choi, "Voltage Sag Compensation with Energy Optimized Dynamic Voltage Restorer", IEEE Trans. on Power Delivery, Vol. 18, No. 3, pp. 928-936, July 2003.

[5] H. Kim, "Minimal Energy Control for a Dynamic Voltage Restorer", IEEE PCC Conference, Vol. 2, Osaka, Japan, pp. 428-433, 2002.

[6] V.K. Ramachandaramurthy, C. Fitzer, A. Arulampalam, C. Zhan, M. Barnes, N. Jenkins, "Control of a Battery Supported Dynamic Voltage Restorer", IEE Proceedings - C, Generation Transmission and Distribution, Vol. 149, No. 5, 2002.

[7] M.R. Banaei, S.H. Hosseini, S. Khanmohamadi, G.B. Gharehpetian, "Verification of a New Energy Control Strategy for Dynamic Voltage Restorer by Simulation", Simulation Modeling Practice and Theory, pp. 112-125, 2006.

[8] Manitoba HVDC Research Center, "PSCAD/EMTDC: Electromagnetic Transients Program Including DC Systems", 1994.

[9] G. Spiazzi, P. Mattavelli, L. Rossetto, L. Malesani, "Application of Sliding Mode Control to Switch Mode Power Supplies", Journal of Circuits, Systems and Computers, Vol. 5, No. 3, pp. 337-354, September 1995.

[10] H. Pinheiro, A.S. Martins, J.R. Pinheiro, "A Sliding Mode Controller in Single Phase Voltage Source Inverters", International Conference on Industrial Electronics, Control, and Instrumentation, pp. 394-398, 1994.

[11] J. Fernando Silva, "Control Methods for Power Converters", Handbook of Power Electronics, M.H. Rashid (Editor), pp. 431-486, 2002.

[12] V. Utkin, "Variable Structure Systems with Sliding Mode", IEEE Trans. on Automatic Control, Vol. AC-22, No. 2, pp. 212-222, April 1977.

[13] G. Spiazzi, "Application of Sliding Mode Control to Switch Mode Power Supplies", Department of Electrical Engineering, University of Padova, [www.unipd.it](http://www.unipd.it).

[14] S.S. Choi, B.H. Li, D.D. Vilathgamuwa, "Dynamic Voltage Restoration with Minimum Energy Injection", IEEE Trans. on Power Syst., Vol. 15, pp. 51-57, Feb. 2000.

[15] O. Anaya Lara, E. Acha, "Modeling and Analysis of Custom Power Systems by PSCAD/EMTDC", IEEE Trans. on Power Del., Vol. 17, No. 1, pp. 266-272, January 2002.

[16] S.F. Torabi, D. Nazarpour, Y. Shayestehfar, "Compensation of Sags and Swells Voltage Using Dynamic Voltage Restorer (DVR) during Single Line to Ground and Three-Phase Faults", International Journal on Technical and Physical Problems of Engineering (IJTPE), Issue 12, Vol. 4, No. 3, pp. 126-132, September 2012.

[17] M. Karimian, A. Jalilian, "Proportional Repetitive Control of a Dynamic Voltage Restorer (DVR) for Power Quality Improvement", International Journal on Technical and Physical Problems of Engineering (IJTPE), Issue 11, Vol. 4, No. 2, pp. 18-23, June 2012.

## BIOGRAPHIES



**Murtaza Farsadi** was born in Khoy, Iran, in September 1957. He received his B.Sc. degree in Electrical Engineering, M.Sc. degree in Electrical and Electronics Engineering and Ph.D. degree in Electrical Engineering (High Voltage) from Middle East Technical University, Ankara, Turkey in 1982, 1984 and 1989, respectively. He is now an Assistant Professor in the Electrical Engineering Department, Urmia University, Urmia, Iran. His main research interests are in high voltage engineering, industrial power system studies and FACTS, HVDC transmission systems and DC/AC active power filters.



**Jafar Mostafapour** was born in Urmia, Iran, in 1974. He received the B.Sc. degree in Electrical Engineering in 2013. His research interests are in advanced power electronic and FACTS, partial discharge classification and localization in power transformers, fault location and HVDC transmission system and DC/AC active power filters.



**Ebrahim Ogabi** was born in Urmia, Iran, in 1978. He received the B.Sc. degree in Electrical Engineering in 2013. His research interests are in advanced power electronic and FACTS, partial discharge classification and localization in power transformers, fault location and HVDC transmission system.